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Docket No.: 60188-734

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

Customer Number: 20277

Makoto KITABATAKE, et al.

Confirmation Number: 4123

Serial No.: 10/736,672

Group Art Unit: 2811

Filed: December 17, 2003

Examiner: Unknown

For:

SEMICONDUCTOR DEVICE AND SUSTAINING CIRCUIT

#### REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop OFR Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the First Inventor's name is incorrect. Attached is a copy of the Declaration and Power of Attorney Document, which evidences that the First Inventor's name should read: Makoto Kitabatake. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E. Fogarty Registration No. 36,139

600 13th Street, N.W. Washington, DC 20005-3096 (202) 756-8000 MEF:tlb Facsimile: (202) 756-8087

Date: April 6, 2004



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**CONFIRMATION NO. 4123** 

Jack Q. Lever, Jr. McDERMOTT, WILL & EMERY 600 Thirteenth Street, N.W. Washington, DC 20005-3096

**FILING RECEIPT** 

\*OC000000012175038\*

Date Mailed: 03/24/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) Makoto

Mekoto Kitabatake, Nara, JAPAN; Kazuhiko Asada, Osaka, JAPAN; Hidekazu Yamashita, Osaka, JAPAN; Nobuyoshi Nagagata, Kyoto, JAPAN; Kazuhiro Nobori, Osaka, JAPAN; Hideki Omori, Hyogo, JAPAN; Masanori Ogawa, Shiga, JAPAN;

**Assignment For Published Patent Application** 

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.;

Domestic Priority data as claimed by applicant

Foreign Applications

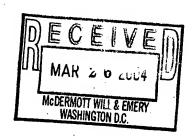
JAPAN 2002-367848 12/19/2002

If Required, Foreign Filing License Granted: 03/24/2004

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Non-Publication Request: No

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Semiconductor device and sustaining circuit

**Preliminary Class** 

257

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Docket No.	
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## COMBINED DECLARATION/POWER OF ATTORNEY FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

is claimed and for	d joint inventor (if plu	and sole inventor (if only one in oral names are listed below) of the on the invention entitled	name is listed below) or the subject matter which
	DEVICE AND SUST		•
			pecification of which
(check one)	X is attached	hereto.	
	was filed on Application	Serial No.	as
		ed and understand the contents tended by any amendment refer	
		se information which is materia 37, Code of Federal Regulations	
any foreign applic	cation(s) for patent o	benefits under Title 35, United or inventor's certificate listed for patent or inventor's certific iority is claimed:	below and have also
Prior Foreign Appli	antion(n)		Priority Claimed
	cation(s)		
2002-367848 (Number)		19/12/2002 (Day/Month/Year Filed)	X Yes No
	JAPAN		Yes No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Appln. Serial No.)	(Filing Date)	(Status-patented, pending, abandoned)		
(Appln. Serial No.)	(Filing Date)	(Status-patented, pending, abandoned)		

I hereby appoint as my attorneys, with full power of substitution and revocation to prosecute the patent application identified above and to transact all business in the U.S. Patent and Trademark Office connected therewith: Raphael V. Lupo (Reg. No. 28,363); Jack Q. Lever, Jr. (Reg. No. 28,149); Kenneth L. Cage (Reg. No. 26,151); Stanislaus Aksman (Reg. No. 28,562); Paul Devinsky (Reg. No. 28,553); Edward E. Kubasiewicz (Reg. No. 30,020), Michael E. Fogarty (Reg. No. 36,139); Brian E. Ferguson (Reg. No. 36,801); Robert W. Zelnick (Reg. No. 36,976); and Wilhlem F. Gadiano (Reg. No. 37,136).

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The undersigned hereby authorizes the U.S. attorneys named herein to accept and follow instructions from Maeda Patent Office as to any action to be taken in the Patent and Trademark Office regarding this application without direct communication between the U.S. attorney and the undersigned. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by the undersigned.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United Sates Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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<sup>\*</sup> City and State, or City and Country for foreign inventors